Hybrid Workshop

Trusted Chips: The Standardization Landscape & Opportunities for Europe
2nd December 2022

PROGRAMME

[#Standards4Chips]

Venue: CEN and CENELEC Meeting Centre (Rue de la Science 23 – 1040, Brussels, Belgium) / Online	
Moderator: Giovanni COLLOT, CEN and CENELEC	
8:30 – 9:00	**GET SETTLED IN (IN-PERSON/ONLINE)**
9:00 – 9:15	**OPENING WORDS: TRUSTED CHIPS** *Welcoming words from the European Standardization Organizations, CEN and CENELEC, to set the scene on the strategic value of standardization in support of the Chips Act, and security of the Chips* ▶ **Elena SANTIAGO CID,** Director General, CEN and CENELEC ▶ **Frédéric VAILLANT,** Vice-President Technical, CENELEC
9:15 – 9:30	**KEYNOTE SPEECH: THE EUROPEAN SEMICONDUCTOR STRATEGY** ▶ **Colette MALONEY,** Head of Unit CNECT A3, Microelectronics and Photonics Industry, European Commission
9:30 – 10:30	**THREATS ALONG THE ELECTRONICS VALUE CHAIN** *Speakers will provide an overview of the electronics value chain and threats along it, with a focus on trusted electronics, the semiconductor standardization focus areas and main existing activities at the international and European level around semiconductor devices.* ▶ **Nooshin AMIRIFAR,** Project Manager, CEN and CENELEC ▶ **Marnix VLOT**, Standardization Business Partner, NXP ▶ **Werner BERNS,** MGTS & International Standardization & High-Speed Video Link, Texas Instruments ▶ **Andreas ZANKL**, Security Researcher, Fraunhofer Institute for Applied and Integrated Security (AISEC) **PANEL DISCUSSION, Q&A**
10:30 – 11:15	**WHAT ARE TRUSTED CHIPS?** *Speakers will provide a detailed and informative description of trusted chips and present the different categories of threats and protections in their development.* ▶ **Olivier VAN NIEUWENHUYZE**, Security Standardization Senior manager, STMicroelectronics

	▶ **John BOGGIE**, Director, Head of Cybersecurity Certification, NXP Semiconductors ▶ **Paulo VALENTE,** Chief executive officer of SmartLampPost, Member of European Digital SME Alliance PANEL DISCUSSION, Q&A
11:15 -11:30	*COFFEE BREAK*
11:30 – 12:45	DEEP DIVE: STANDARDIZATION AND SECURITY CERTIFICATION FOR TRUSTED CHIPS *The presentations of this section will describe the current standardization and security certification landscape and identify emerging opportunities and priorities to support the development of trusted chips in response to the relevant European policy priorities.* ▶ **Jose Manuel PULIDO,** Cybersecurity Consultant Leader & Senior Cybersecurity Evaluator, JTSEC ▶ **Mariela PAVLOVA,** Senior Security Certification Expert, Infineon Technologies ▶ **Sławomir GÓRNIAK,** Senior Cybersecurity Expert, European Union Agency for Cybersecurity (ENISA) ▶ **Miguel BAÑÓN,** Convenor of ISO/IEC JTC 1/SC 27/WG 3 'Security evaluation, testing and specification', International Electrotechnical Commission (IEC) PANEL DISCUSSION, Q&A
12:45 - 13:00	CLOSING REMARKS *Wrap-up of the day and next steps for CEN and CENELEC* ▶ **Celine OEYEN,** Manager EU Policy and Stakeholders, DKE Brussels Representation ▶ **Stipe MANDIC,** Project Manager Energy, DKE
13:00 – 16:00	*NETWORKING LUNCH & DISCUSSION ON NEXT STEPS*